

Questa Product Comparison			
Feature	ModelSim SE	Questa SV	Questa AFV
General			
Licensing - Floating License	■	■	■
Language Neutral License	Option		
ASIC Sign-Off	■	■	■
HDL Editor	■	■	■
Integrated Project Manager	■	■	■
Source Code Templates and Wizards	■	■	■
Platform-Independent Compiled Database	■	■	■
Native-Compiled Architecture	■	■	■
Incremental Compilation	■	■	■
32/64-Bit Cross-Compatibility	■	■	■
Languages			
VHDL	■		■
Verilog	■	■	■
Mixed Language	Option		■
Verilog 2001	■	■	■
SystemVerilog 3.1a Design	■	■	■
SystemVerilog 3.1a Testbench		■	■
SystemVerilog 3.1a Assertions		■	■
PSL Assertions			■
SystemC 2.1	Option	Option	■
Analog/Mixed Signal (Advance MS Product)	Option	Option	Option
Verilog PLI/VPI	■	■	■
SystemVerilog Direct Programming Interface	■	■	■
VHDL FLI	■		■
Debug			
Source Code Debugging	■	■	■
Dataflow Window	■	■	■
C Debugger	Option (w/ SystemC)	■	■
Memory Window	■	■	■
Extra Standalone Viewer	Option	Option	Option
Multiple Waveform Windows	■	■	■
Waveform Compare	■	■	■
JobSpy	■	■	■
SignalSpy	■	■	■
User-Customizable GUI (via Tk)	■	■	■
Code Coverage (with Toggle Coverage)	■	■	■
SystemVerilog Functional Coverage		■	■
PSL Functional Coverage			■
Simulation			
Single-Kernel Simulation Engine	■	■	■
Verilog Performance Optimizations	■	■	■
Verilog Gate Optimizations	■	■	■
Performance and Memory Profiler	■	■	■
Separate Elaboration	■	■	■
Waveform Management Tool Set	■	■	■
VCD and Extended VCD Support	■	■	■
VCD Re-simulation	■	■	■
Batch Mode Simulation	■	■	■
Integrated Sim Farm Support (via JobSpy)	■	■	■
Interactive Simulation	■	■	■
Checkpoint & Restore	■	■	■
SWIFT Interface / SmartModels	■	■	■
Synopsys Hardware Modeler Support	■	■	■
Platform Support			
32-Bit OS Support	AIX, HP-UX, Linux, Solaris, Windows 98/NT/ME/2000/XP	AIX, HP-UX, Linux, Solaris, Windows 98/NT/ME/2000/XP	AIX, HP-UX, Linux, Solaris, Windows 98/NT/ME/2000/XP
64-Bit OS Support	AIX, HP-UX, Solaris, Linux (Itanium-2, Opteron)	AIX, HP-UX, Solaris, Linux (Itanium-2, Opteron)	AIX, HP-UX, Solaris, Linux (Itanium-2, Opteron)